The Annual IEEE/IFIP International Conference on Dependable Systems and Networks (DSN) has pioneered the fusion between dependability and security research, understanding the need to simultaneously fight against accidental faults, intentional cyber-attacks, design errors, and unexpected operating conditions. Its distinctive approach to both accidental and malicious faults made DSN the most prestigious international forum for presenting research furthering robustness and resilience of today’s wide spectrum of computing systems and networks, with dependability embracing security aspects under a common body of knowledge.

All aspects on the research and practice of dependability and applied security are within the scope of DSN, including innovative architectures, protocols and algorithms, as well as experimentation with and assessment of dependable and secure systems and networks. Authors are invited to submit original papers on research and practice of creating, validating, deploying, and maintaining dependable and secure systems and networks.

The program will include plenary talks, regular papers, experience reports, an industrial track, a student forum, a fast abstracts track, together with state-of-the-art workshops and tutorials. We need your contribution and participation to make it a successful event.

The 2019 edition of the DSN conference will take place in the city of Portland. Located on the banks of the Willamette river, between the Pacific Ocean and Mount Hood, Portland is the cultural and economic heart of the Oregon State. Portland is home to companies like Intel, IBM, and Tektronix, and the Portland State University. It is in driving distance from Seattle, cultural and economic heart of the Oregon State. Portland is home to companies like Intel, IBM, and Tektronix, and the Portland State University. It is in driving distance from Seattle,

We are looking forward to welcoming you in Portland in June 2019.

**Important Dates:**

- Abstract Submission: November 30, 2018
- Full Papers: December 7, 2018
- Acceptance Notification: March 4, 2019

**Organizing Committee**

**General Chairs:** Cristian Constantinescu, AMD, USA

**Program Committee Chairs:** Fernando Pedone, University of Lugano, Switzerland, and Karthik Pattabram University of British Columbia, Canada

**Workshops Chairs:** Matthieu Roy, LAAS, France, and Yennunn Huang, Academia Sinica, Taiwan

**Tutor Chair:** Zbigniew Kalbarczyk, University of Illinois, USA

**Fast Abstract Chair:** Saurabh Bagchi, Purdue University, USA

**Student Forum Chairs:** Christof Fetzer, TU Dresden, Germany, and Homa Alemzadeh, University of Virginia, USA

**Industrial Track Chairs:** Timothy Tsai, NVIDIA, USA, and Tudor Dumitras, University of Maryland, USA

**Publication Chair:** Miguel Correia, INESC-ID / IST, Portugal

**Registration Chair:** David Taylor, University of Waterloo, Canada

**Publicity Chairs:** Karama Kanoun, LAAS-CNRS, France, Kazuhiko Iwasaki, Tokyo Metropolitan University, Japan, and Robert Swarz, Worcester Polytechnic Institute, USA

**Webmaster:** Ibenia Medeiros, University of Lisbon, Portugal

More information at: [www.dsn.org](http://www.dsn.org)

**Program Committee Members**

Alexey Gotsman, IMDEA Software Institute, Spain

Alysson Bessari, University of Lisbon, Portugal

Andrey Brito, UFCLG, Brazil

Benoit Garbiniato, University of Lausanne, Switzerland

Christian Cachin, IBM Research Zurich, Switzerland

Christian Cachin, IBM Research Zurich, Switzerland

David Kozhaya, ABB Corporate Research, Switzerland

Devesh Tiwari, Northeastern University, USA

Domenico Cotroneo, University of Naples, Italy

Doug Seong Kim, University of Queensland, Australia

Elena Troubitsyna, Abo Akademii, Finland

Emilozzabelaia Einozayo, KAUST, Saudi Arabia

Etienne Riviere, UCLouvain, Belgium

Evgenia Smirni, College of William and Mary, USA

Felicitas Di Giandomenico, ISTI-CNR, Italy

Fernando Dott, PUC-BrS, Brazil

Francois Taiani, Univ. Rennes, CNRS, Inria, France

Frolin Ocaniza, SAP, Canada

Gabriela Jacques Da Silva, Facebook, USA

Hemng Cui, University of Hong Kong, China

Homa Alemzadeh, University of Virginia, USA

Ilir Gashi, City University of London, UK

Jose Orlando Pereira, INESC TEC & U. Minho, Portugal

Jussara M. Almeida, UFMG, Brazil

Kenji Kono, Keio University, Japan

Keun Soo Yim, Google, USA

Kong Wang, IBM Research, USA

Luis Rodrigues, Universidade do Lisboa, Portugal

Lydia Y. Chen, ETH Zurich, Switzerland

Marco Serafini, University of Massachusetts, USA

Marco Vieira, University of Coimbra, Portugal

Mattan Erez, University of Texas at Austin, USA

Michael Lyu, Chinese University of Hong Kong, China

Miguel Correia, Instituto Superior Tecnico, Portugal

Mohamed Kaaniche, LAAS, France

Nathan DeBarbarde, Los Alamos National Labs, USA

Neej Suria, T U Darmstadt, Germany

Nuno Neves, University of Lisbon, Portugal

Onur Mutlu, ETH Zurich, Switzerland

Paolo Rech, UFRGS, Brazil

Paolo Romano, University of Lisbon & INESC-ID, Portugal

Parisa Marandi, Microsoft, UK

Pascal Felber, UNINE, Switzerland

Peter Pietzuch, Imperial College London, UK

Rakesh Babba, Oregon State University, USA

Regina Moraes, University of Campinas, Brazil

Ric Schlichting, US Naval Academy, USA

Rudiger Kapitz, TU Braunschweig, Germany

Saarman Zonouz, Rutgers, USA

Samet Elnikety, Microsoft Research, USA

Santonu Sarkar, ABB Research, India

Sara Bouchnak, INSA Lyon, France

Saurabh Bagchi, Purdue University, USA

Shuo Chen, Microsoft Research, USA

Susanna Donatelli, University of Torino, Italy

Timothy Tsai, Nvidia, USA

Vana Kalogeraki, Athens Univ. of Economics & Business, Greece

Vilas Shridharan, AMD, USA

William H. Sanders, University of Illinois, USA

Yennunn Huang, Academia Sinica, Taiwan

Zbigniew Kalbarczyk, University of Illinois, USA

**Sponsored by:**

[IEEE Computer Society](http://www.computer.org)

[IFIP](http://www.ifip.org)